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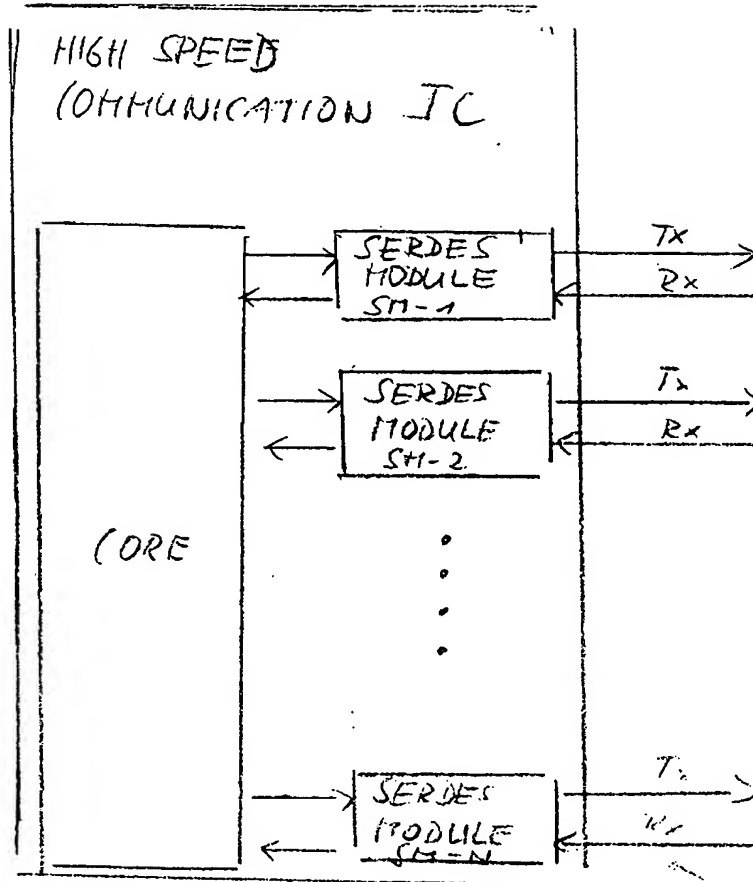


Fig 1

State of  
the art

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State of the  
art

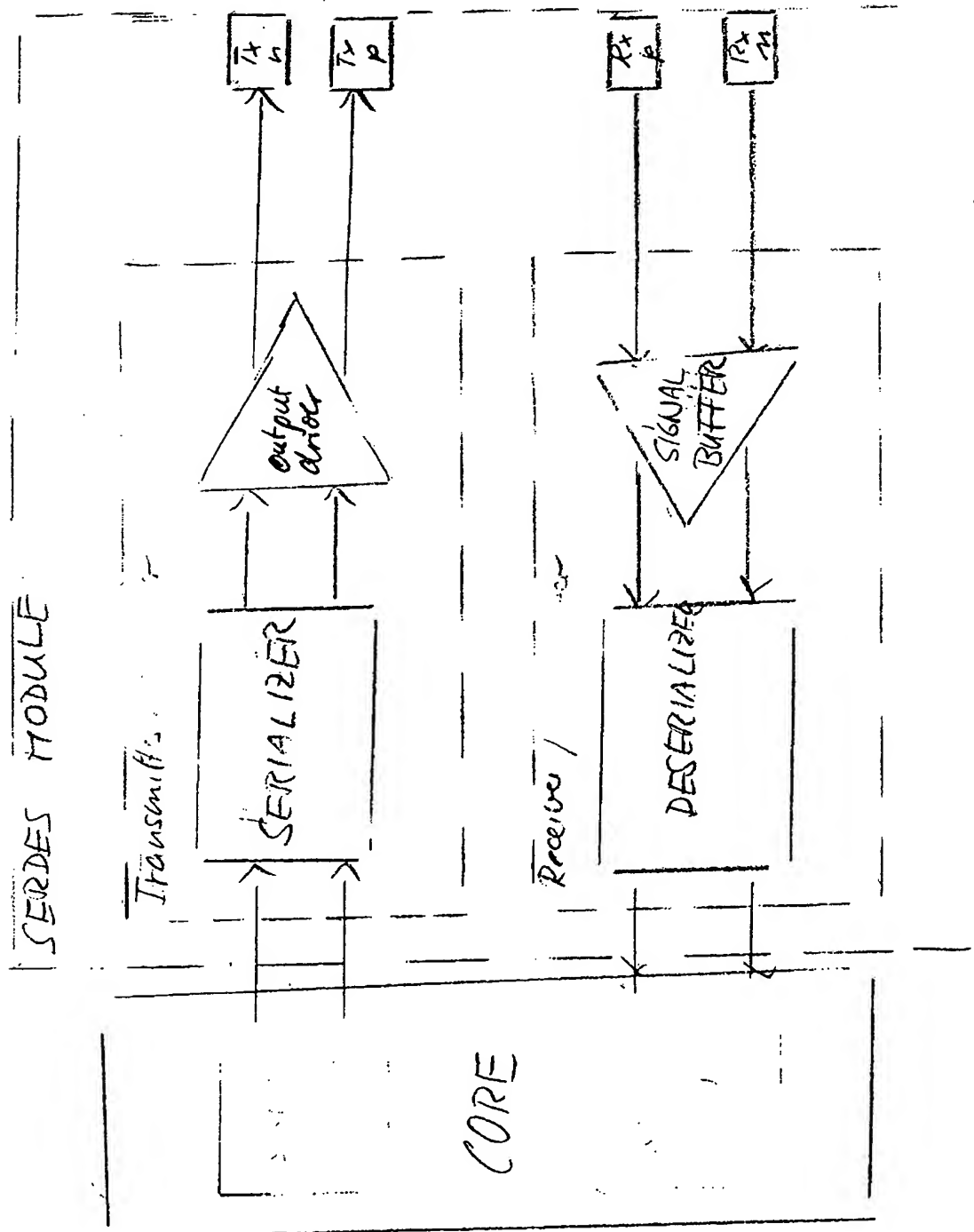


Fig 2

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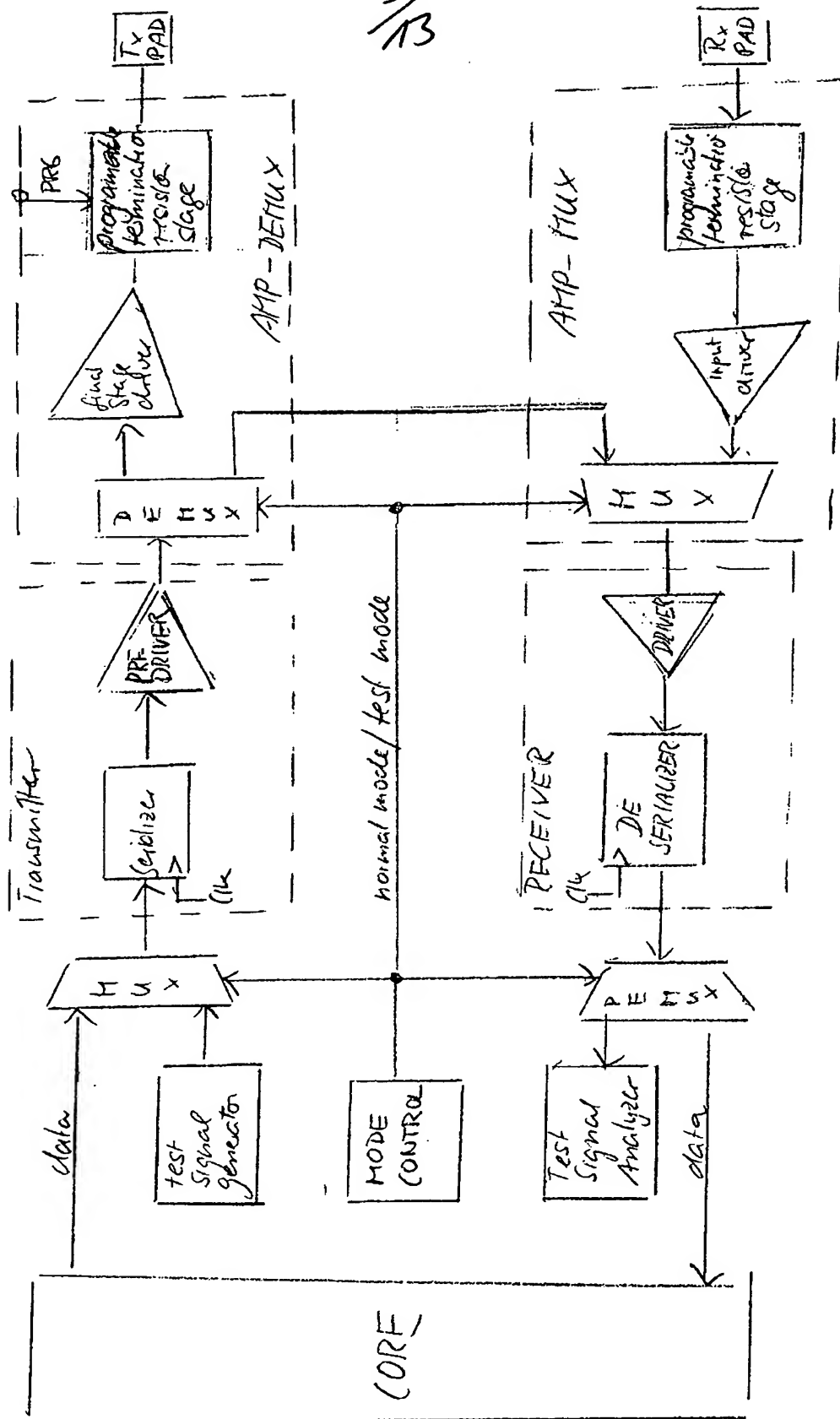


Fig. 3  
State of  
the art

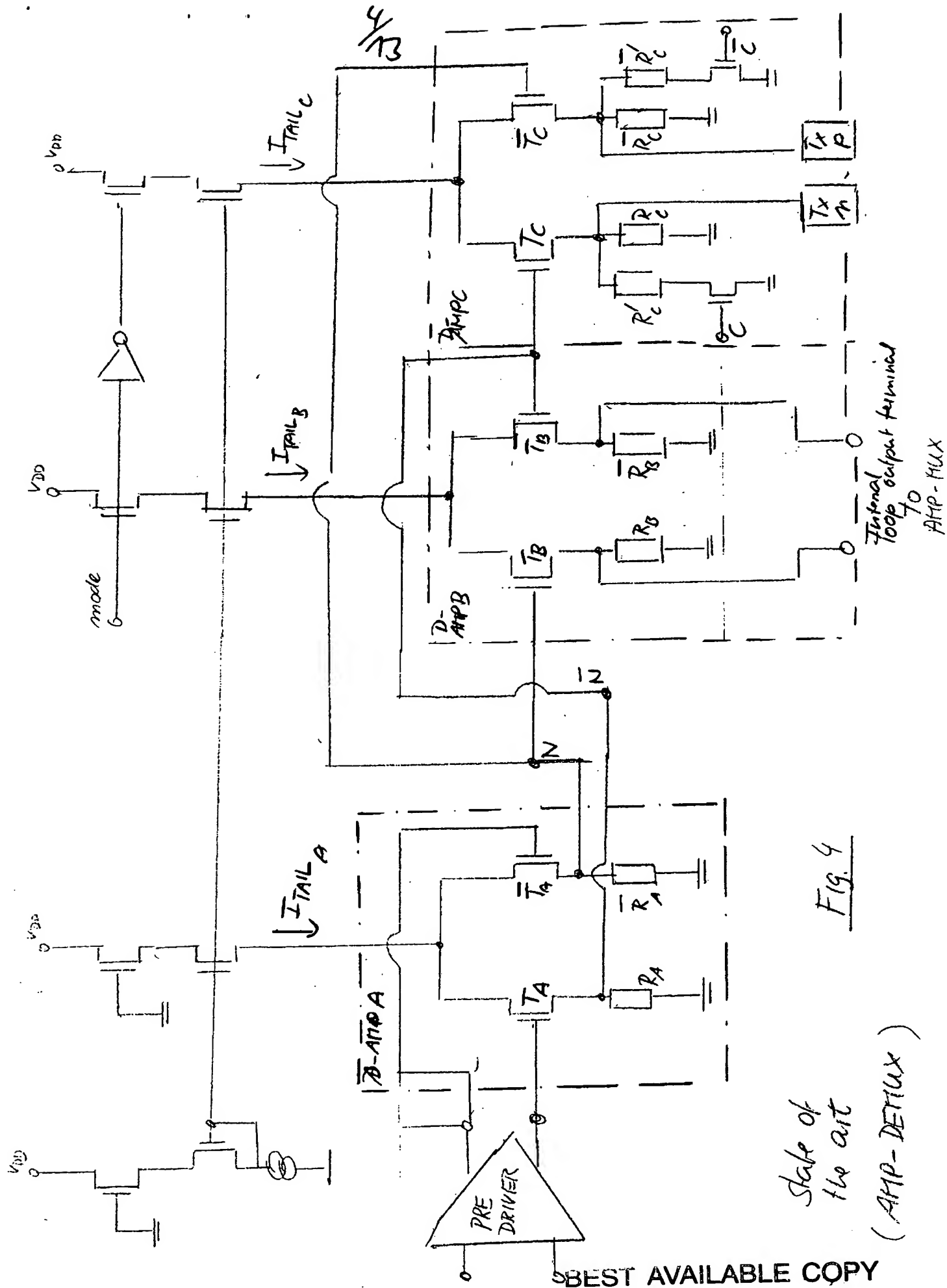
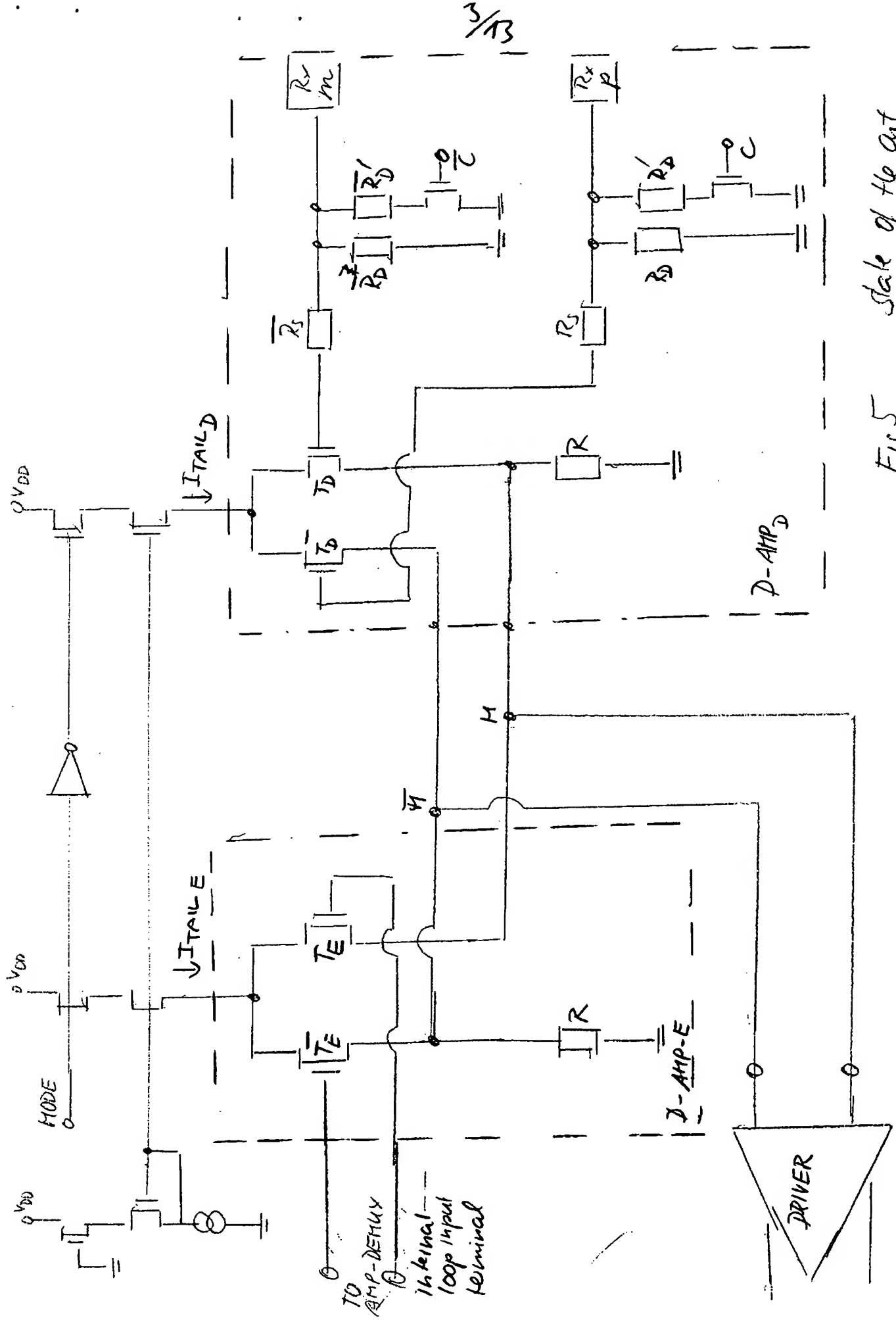


Fig. 4

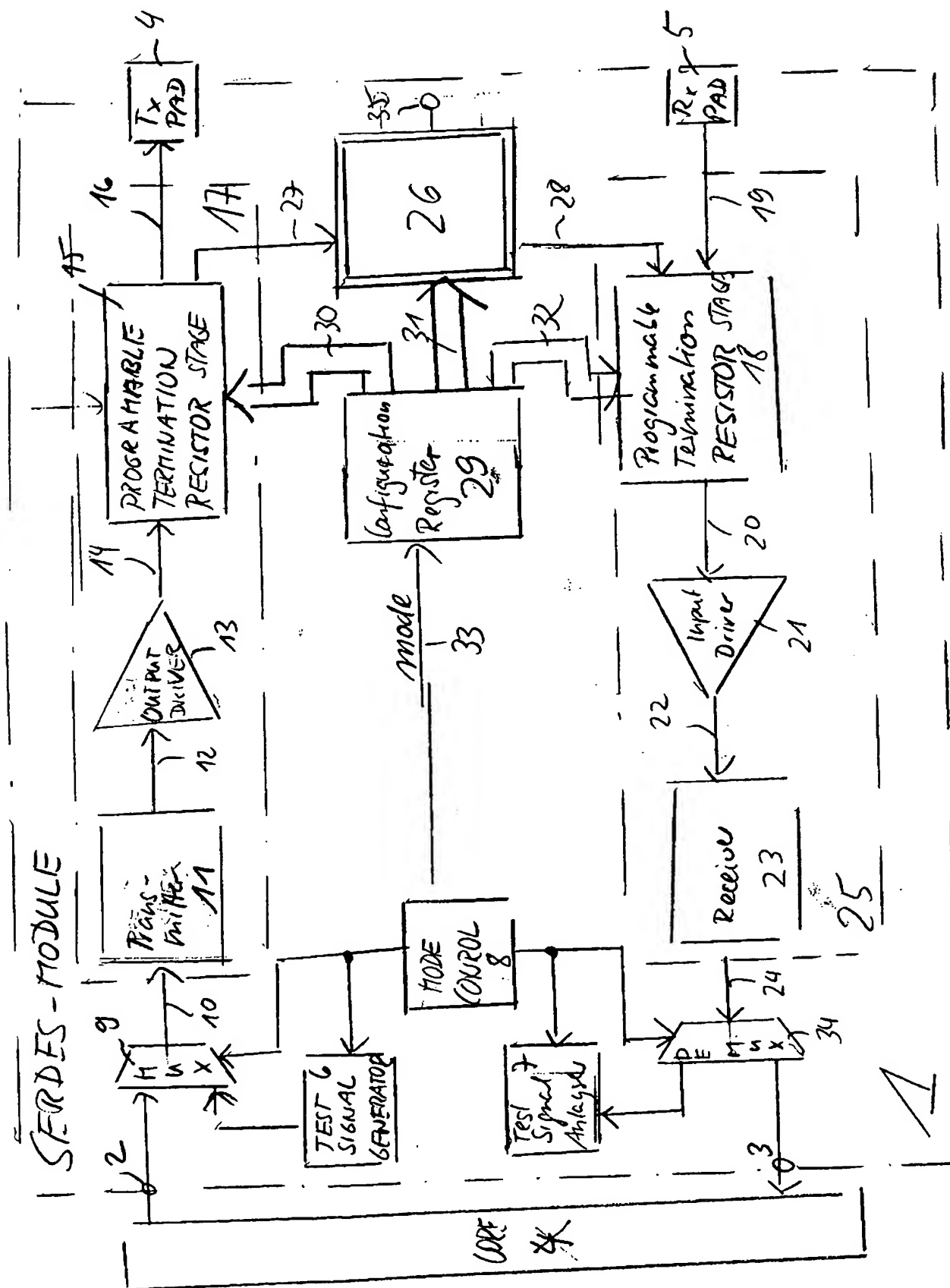
State of  
the art  
(AMP-DETUX)



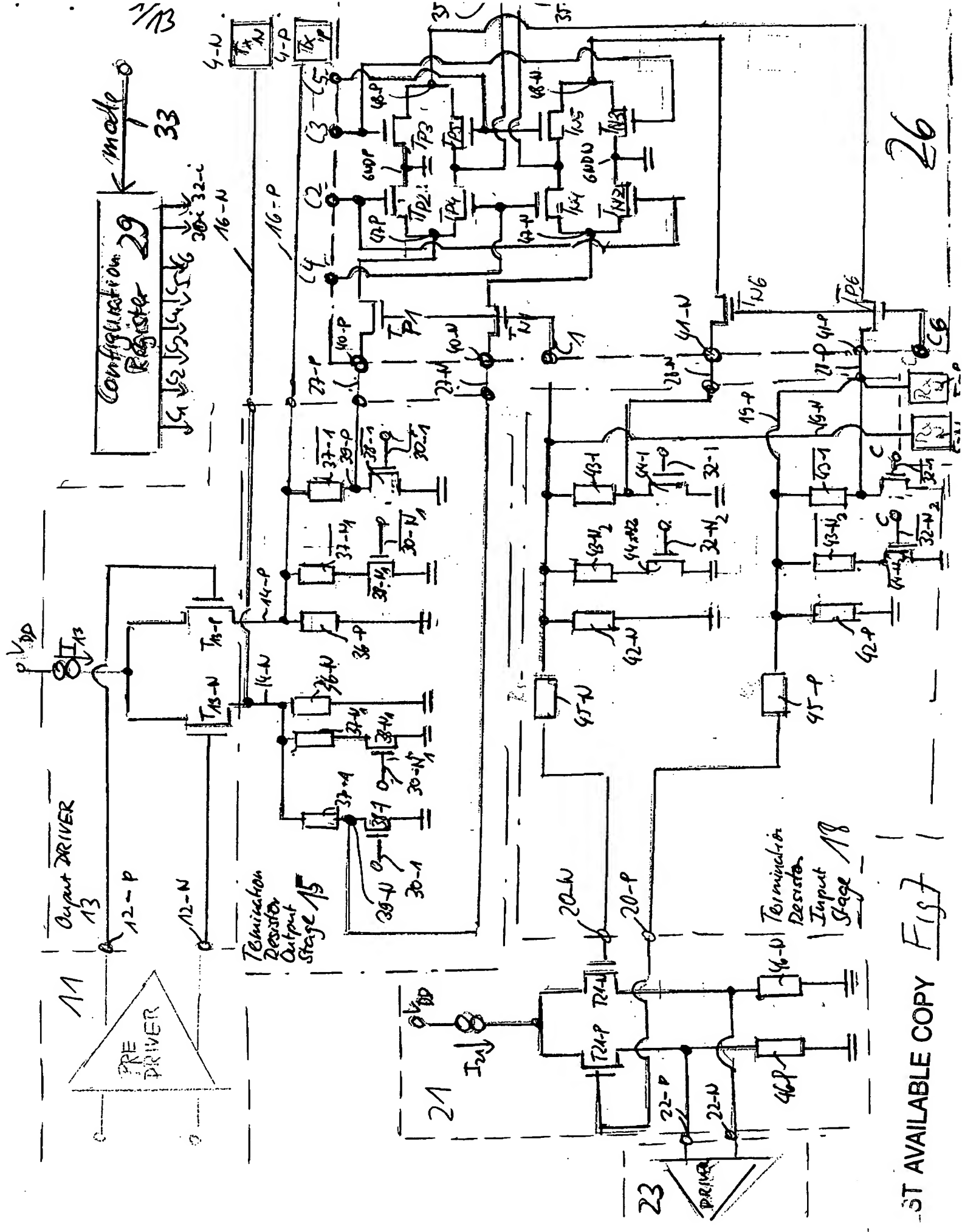
State of the Art  
(AMP-Mux)

Fig 5

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$$\frac{6}{13}$$
Fig 6

**BEST AVAILABLE COPY**



ST AVAILABLE COPY Fig 7

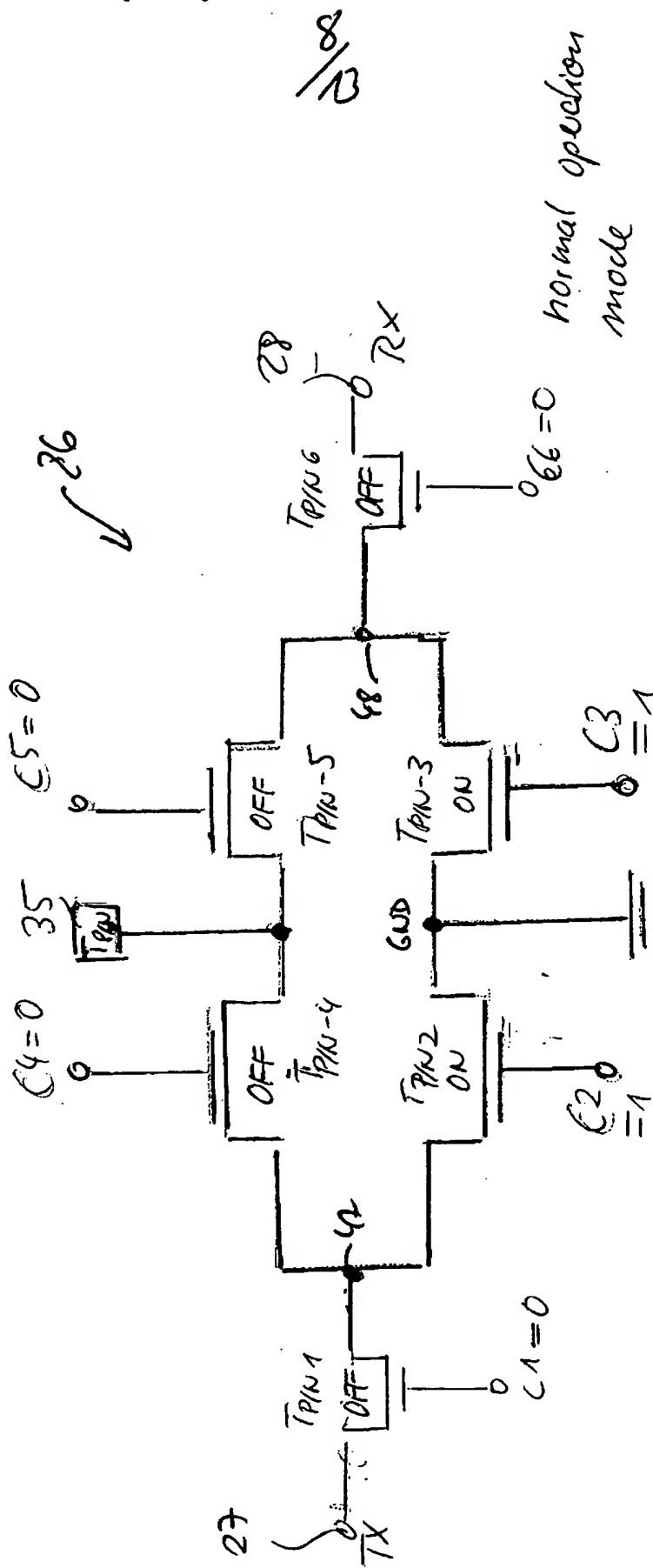


Fig 8



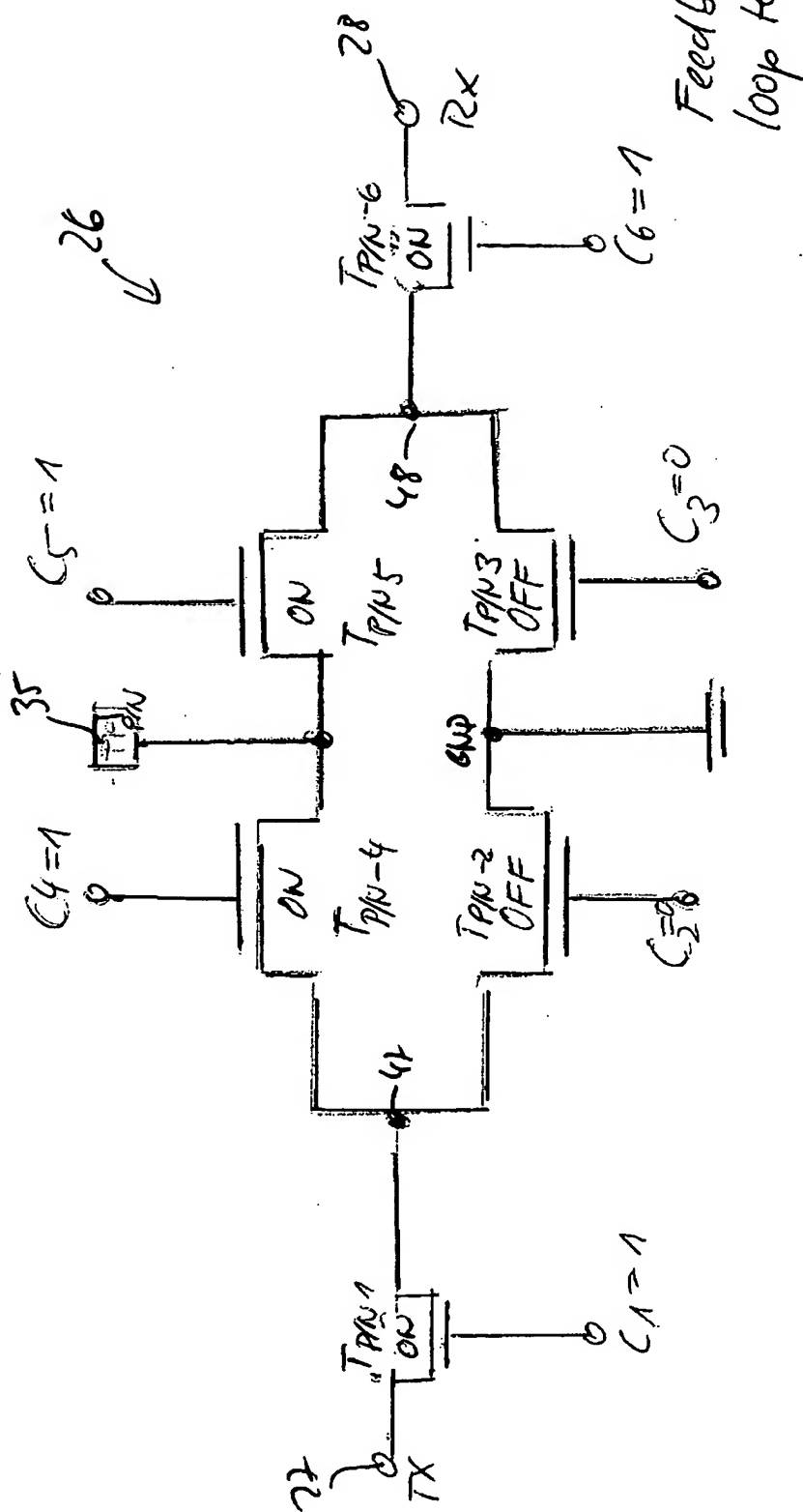
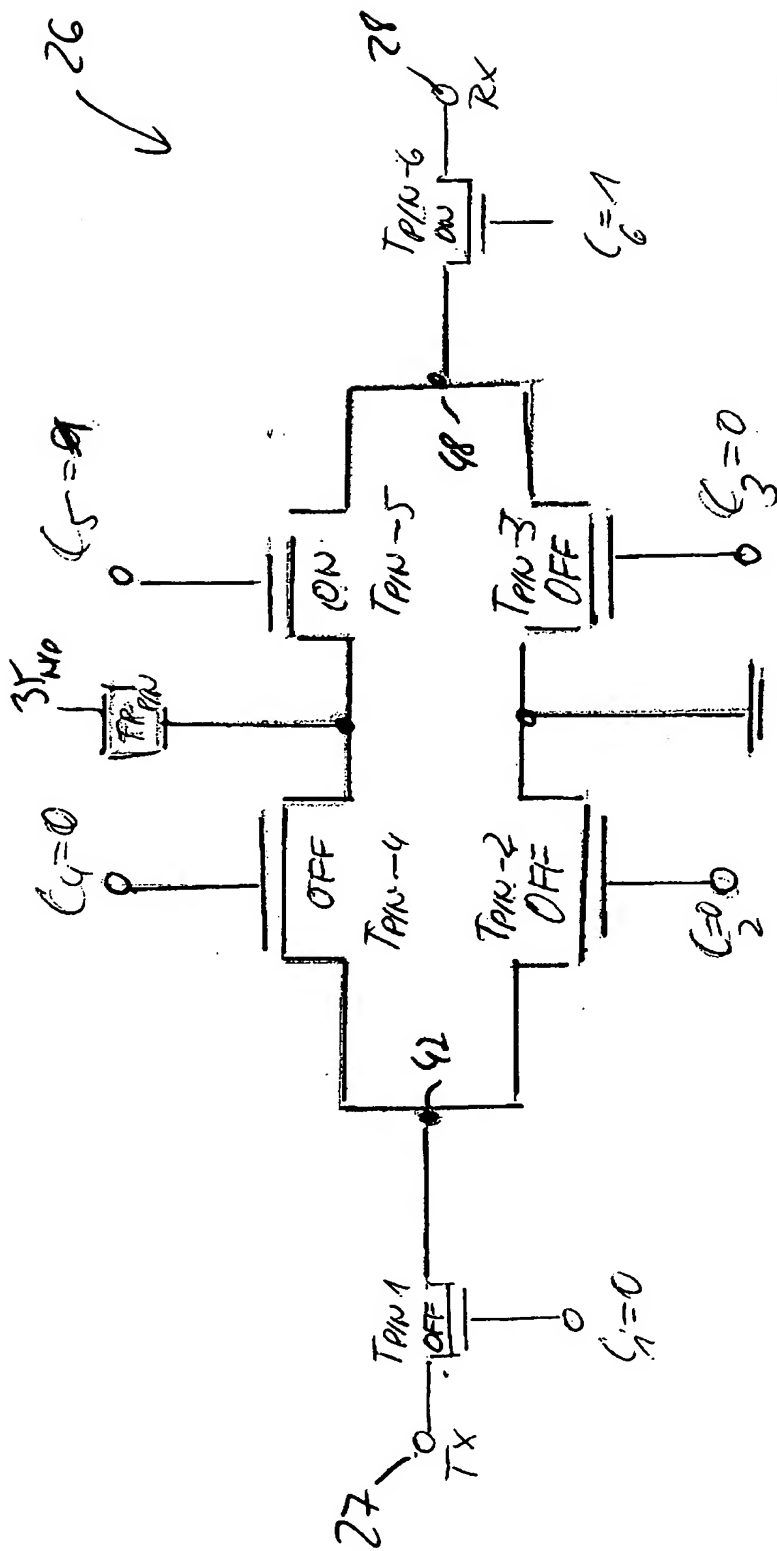
$$\frac{9}{13}$$


Fig.

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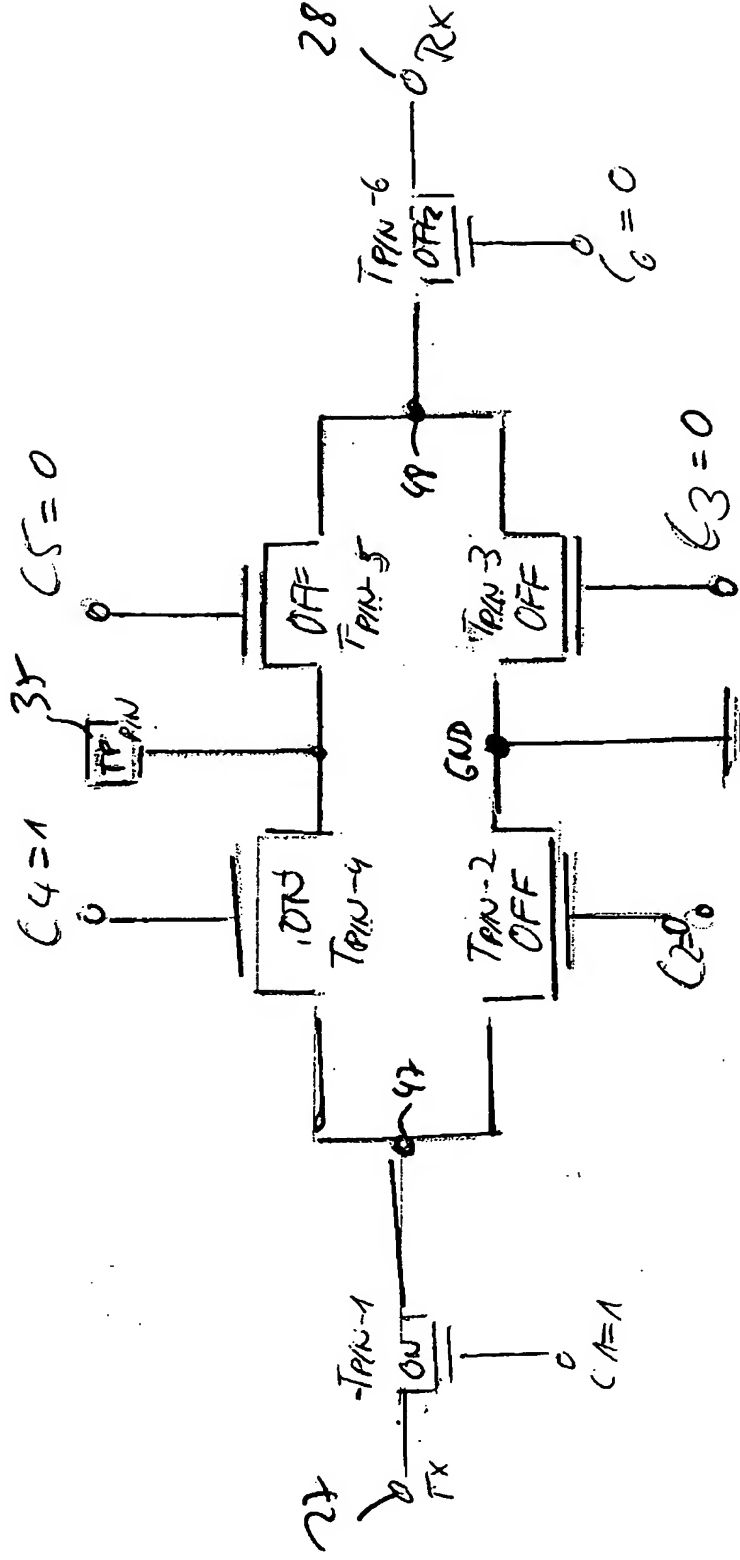


Receiver  
test mode

Fig 90

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M/13



transmitter  
test mode

Fig. A1

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# Configuration Register

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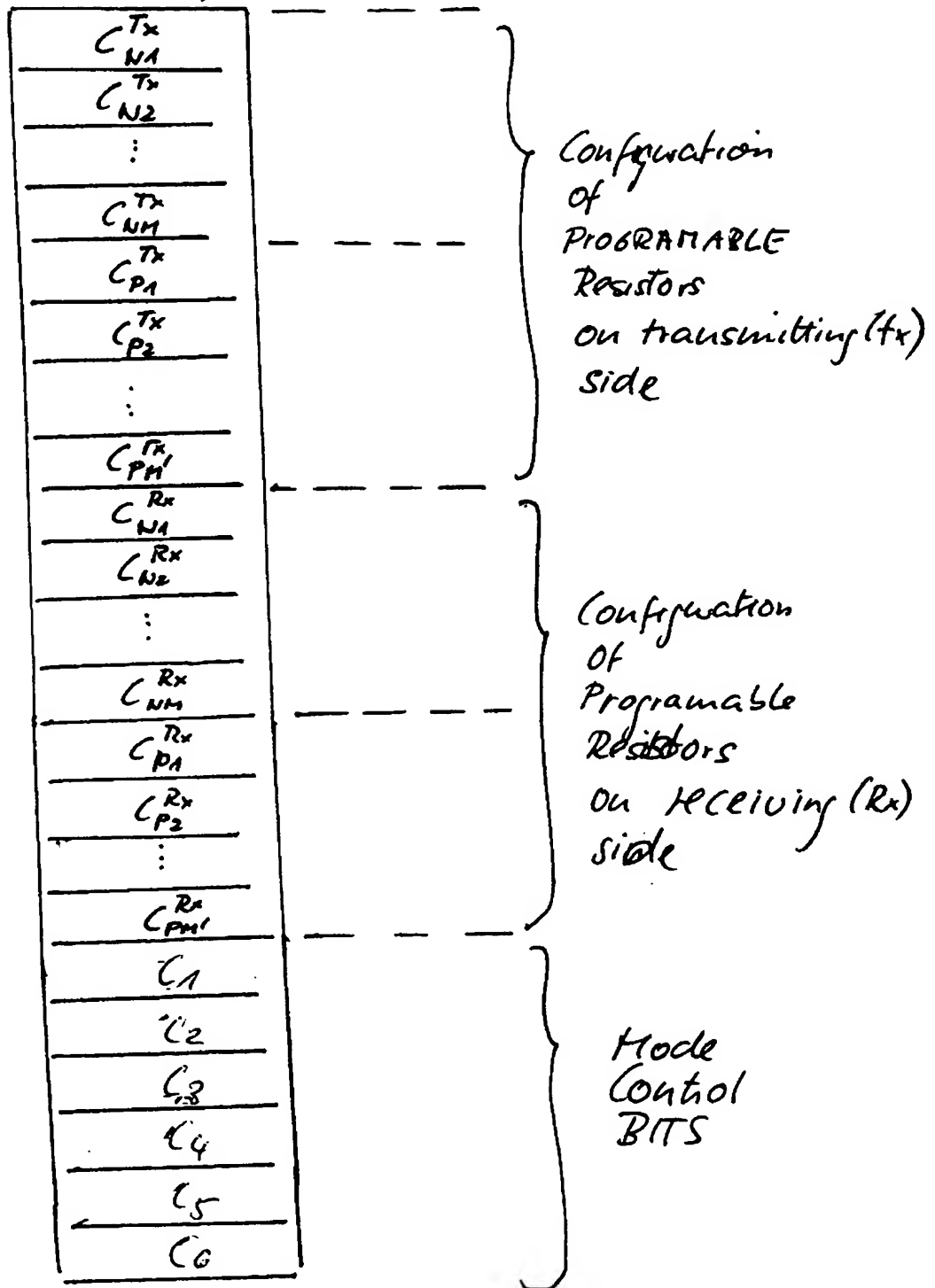


Fig 12

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Control BITS	Feedback		Receive	Transmit
	Normal operation MODE	Test Mode	Test MODE	TEST MODE
C <sub>1</sub>	0	1	0	1
C <sub>2</sub>	1	0	0	0
C <sub>3</sub>	1	0	0	0
C <sub>4</sub>	0	1	0	1
C <sub>5</sub>	0	1	1	0
C <sub>6</sub>	0	1	1	0

Fig 13